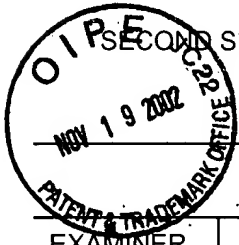


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PATENT AND TRADEMARK OFFICEATTY. DOCKET NO. .  
174/183APPLICATION NO.  
09/805,843SECOND SUPPLEMENTAL INFORMATION DISCLOSURE  
STATEMENT BY APPLICANTSAPPLICANTS  
Edward Aung et al.CONFIRMATION NO.  
3795FILING DATE  
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2182

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>EH</i>	5,802,103	09/01/98	Jeong	375	220	
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## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

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<i>EH</i>	Lemme, Helmuth, "Schnelle Chips Für Flaschenhalse" Elektronik, Franzis Verlag GMBH. Munchen, DE, vol. 40, no. 22, October 29, 1991, pp. 104-109.
<i>//</i>	Cook, Barry M., "IEEE 1355 Data-Strobe Links: ATM Speed at RS232 Cost", Microprocessors and Microsystems, March 30 1998, Elsevier, UK, vol. 21, no. 7-8, pp. 421-428.
<i>//</i>	Konstas, Jason, "Converting Wide, Parallel Data Buses to High Speed Serial Links", Cypress Semiconductor, 1999, pp. 19-30.

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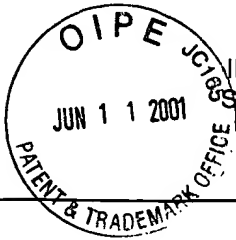
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<i>EB</i>	"Protocol Independent Gigabit Backplane Transceiver Using Lucent ORT4622/ORT8850 FPSCs", Application Note, June 2000, Lucent Technologies Inc., pp. 1-10.
<i>1</i>	"Lucent Introduces 10Gb/s Ethernet FPGAs", Programmable Logic News and Views, Electronic Trend Publications, Inc., Vol. IX, No. 11, November 2000, pp. 7-8.
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## APPLICANTS

Edward Aung, Henry Lui, Paul Butler, John  
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<i>E.B.</i>	3,473,160	10/14/69	Wahlstrom			
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